



FIG. 3

CMOS REGION

BIPOLAR REGION

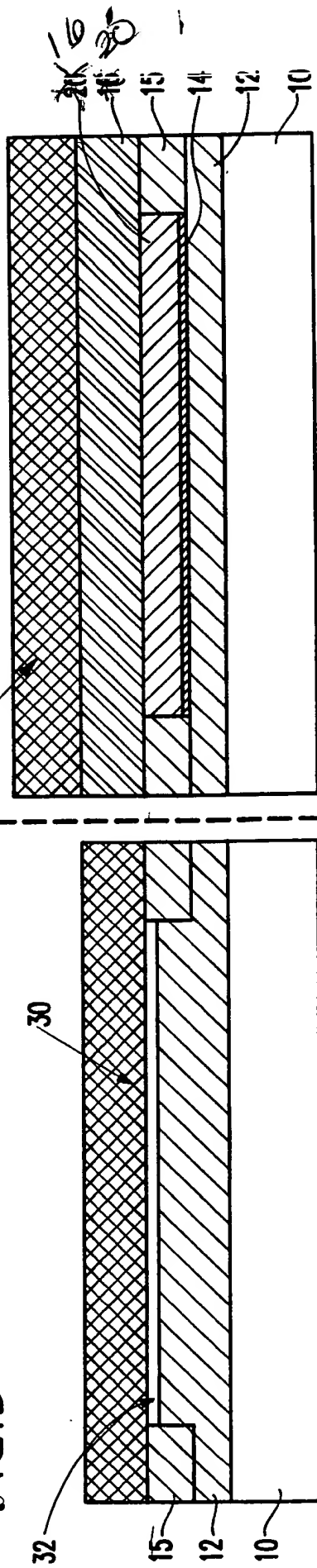


FIG. 4

CMOS REGION

BIPOLAR REGION

